

REMARKS

In the Office Action, the Examiner noted that claims 1-6 are pending in the application. The Examiner allowed claims 1-3, while rejecting claims 4-6. By this Amendment, claims 1, 2, 4 and 6 have been amended without narrowing the claims within the meaning of the Festo case, while new claims 7-13 have been added. Thus, claims 1-13 are pending in the application. The Examiner's rejections are traversed below.

THE SPECIFICATION AND ABSTRACT

In item 1 on page 2 of the Office Action, the Examiner objected to the Abstract. The Abstract has been amended in accordance with the Examiner's comments. In addition, the Applicant has submitted a Substitute Specification in order to make minor amendments to the Specification.

REJECTION UNDER 35 U.S.C. § 101

In item 2 on page 2 of the Office Action, the Examiner rejected claims 4-6 under 35 U.S.C. § 101 because the claimed invention is directed to non-statutory subject matter. In particular, the Examiner took the position that a useful, concrete and tangible result appears to be lacking.

Applicant submits that claims 4-6 meet the requirements of 35 U.S.C. § 101. By way of example and not limitation, claim 4 recites "generating an offset address of butterfly input/output data to read data and write an operated result in a data memory." Such writing of an operated result in a data memory clearly illustrates that claims 4-6 meet the requirements of 35 U.S.C. § 101. Accordingly, it is respectfully requested that the Examiner's rejection be withdrawn.

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

In item 5 on page 3 of the Office Action, the Examiner has provided a Statement of Reasons for Allowance. In order to be clear, the method claim of claim 4 does not recite an "FFT address generator" or an "address generator" as specified in item 5 of the Office Action. Instead, claim 4 recites "generating an offset address . . ."

NEW CLAIMS 7-13

New claims 7-11 depend, directly or indirectly from independent claim 1. New claim 12 is an independent claim directed to a computer-readable storage which recites:

generating a start signal and applying an FFT operation
signal if the FFT starts;
generating an offset address of butterfly input/output data
to read data and write an operated result in a data memory;
storing the generated offset address of the butterfly
input/output data in an offset register of a programmable
processor;
switching data to provide the butterfly input data from the
data memory and write the output data in the data memory;
carrying out a butterfly operation; and
generating an end signal and resetting the FFT operation
signal when the operation is ended.

It is submitted that new claims 7-13 are in condition for allowance.

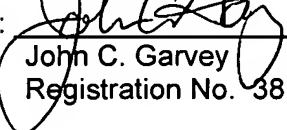
In summary, it is submitted that all claims are in condition for allowance.
Reconsideration of the claims and an early notice of allowance are earnestly solicited.

If there are any additional fees associated with filing of this Preliminary
Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

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